

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A display device comprising:
a source signal line driving circuit;
~~a pixel portion, each of said source signal line driving circuit and said pixel~~
~~portion provided over a substrate;~~
a shift register included in said source signal line driving circuit for serially outputting pulses in accordance with clock signals;
a level shifter included in said source signal line driving circuit for converting a voltage amplitude of input signals; and
a current source provided ~~[[for]]~~ in said source signal line driving circuit for supplying a current to said level shifter,
wherein said current source supplies the current only when said shift register serially outputs the pulses.
2. (Currently Amended) A display device according to claim 1, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.
3. (Currently Amended) A display device according to claim 1, wherein said driving circuit and said pixel portion are provided over a same substrate.
4. (Currently Amended) A display device according to claim 1, wherein said driving circuit and said pixel portion are provided over different substrates.

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5. (Currently Amended) A display device according to claim 1, wherein said display device is a liquid crystal display device.

6. (Currently Amended) A display device according to claim 1, wherein said display device is incorporated into a personal computer.

7. (Currently Amended) A display device according to claim 1, wherein said display device is incorporated into a portable information terminal.

8. (Currently Amended) A display device according to claim 1, wherein said display device is incorporated into a car audio set.

9. (Currently Amended) A display device according to claim 1, wherein said display device is incorporated into a digital camera.

10. (Currently Amended) A display device comprising:
a source signal line driving circuit;
~~a pixel portion, each of said source signal line driving circuit and said pixel~~
~~portion provided over a substrate;~~
first to [[xth]] x-th (x : natural number, $x \geq 2$) units included in said source signal
line driving circuit;
a shift register included in the [[ath]] a-th (a : natural number, $1 \leq a \leq x$) unit for
serially outputting pulses in accordance with clock signals;
a plurality of level shifters included in said [[ath]] a-th unit for converting a voltage
amplitude of input signals; and
an [[ath]] a-th current source provided [[for]] in said [[ath]] a-th unit for supplying a
current to said plurality of level shifters,

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wherein said ~~[[ath]]~~ a-th current source supplies the current to said plurality of level shifters only when said shift register in said ~~[[ath]]~~ a-th unit serially outputs the pulses.

11. (Currently Amended) A display device according to claim 10, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

12. (Currently Amended) A display device according to claim 10, wherein said driving circuit and said pixel portion are provided over a same substrate.

13. (Currently Amended) A display device according to claim 10, wherein said driving circuit and said pixel portion are provided over different substrates.

14. (Currently Amended) A display device according to claim 10, wherein said display device is a liquid crystal display device.

15. (Currently Amended) A display device according to claim 10, wherein said display device is incorporated into a personal computer.

16. (Currently Amended) A display device according to claim 10, wherein said display device is incorporated into a portable information terminal.

17. (Currently Amended) A display device according to claim 10, wherein said display device is incorporated into a car audio set.

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18. (Currently Amended) A display device according to claim 10, wherein said display device is incorporated into a digital camera.

19. (Withdrawn) A display device comprising:
a source signal line driving circuit;
a pixel portion, each of said source signal line driving circuit and said pixel portion provided over a substrate;
first to $[[xth]]$ x-th (x : natural number, $x \geq 2$) units included in said source signal line driving circuit;
a shift register included in the $[[bth]]$ b-th (b : natural number, $1 < b \leq x$) unit for serially outputting pulses in accordance with clock signals,
a plurality of level shifters included in said $[[bth]]$ b-th unit for converting a voltage amplitude of input signals; and
a $[[bth]]$ b-th current source provided for said $[[bth]]$ b-th unit for supplying a current to said plurality of level shifters,
wherein said $[[bth]]$ b-th current source supplies the current to said plurality of level shifters only during a part of the period in which said shift register in the b-1th unit serially outputs the pulses and only when said shift register in said $[[bth]]$ b-th unit serially outputs the pulses.

20. (Withdrawn) A display device according to claim 19, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

21. (Withdrawn) A display device according to claim 19, wherein said driving circuit and said pixel portion are provided over a same substrate.

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22. (Withdrawn) A display device according to claim 19, wherein said driving circuit and said pixel portion are provided over different substrates.

23. (Withdrawn) A display device according to claim 19, wherein said display device is a liquid crystal display device.

24. (Withdrawn) A display device according to claim 19, wherein said display device is incorporated into a personal computer.

25. (Withdrawn) A display device according to claim 19, wherein said display device is incorporated into a portable information terminal.

26. (Withdrawn) A display device according to claim 19, wherein said display device is incorporated into a car audio set.

27. (Withdrawn) A display device according to claim 19, wherein said display device is incorporated into a digital camera.

28. (Withdrawn) A display device comprising:
a source signal line driving circuit;
a pixel portion, each of said source signal line driving circuit and said pixel portion provided over a substrate;
first to $[[xth]]$ x-th (x : natural number, $x \geq 2$) units included in said source signal line driving circuit;
a shift register included in the $[[cth]]$ c-th (c : natural number, $1 \leq c \leq x$) unit for serially outputting pulses in accordance with clock signals,
a plurality of level shifters included in said $[[cth]]$ c-th unit for converting a voltage amplitude of input signals; and

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a [[cth]] c-th current source provided for said [[cth]] c-th unit for supplying a current to said plurality of level shifters,

wherein said [[cth]] c-th current source supplies the current to said plurality of level shifters only during a part of the period in which said shift register in the c+1th unit serially outputs the pulses and only when said shift register in said [[cth]] c-th unit serially outputs the pulses.

29. (Withdrawn) A display device according to claim 28, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

30. (Withdrawn) A display device according to claim 28, wherein said driving circuit and said pixel portion are provided over a same substrate.

31. (Withdrawn) A display device according to claim 28, wherein said driving circuit and said pixel portion are provided over different substrates.

32. (Withdrawn) A display device according to claim 28, wherein said display device is a liquid crystal display device.

33. (Withdrawn) A display device according to claim 28, wherein said display device is incorporated into a personal computer.

34. (Withdrawn) A display device according to claim 28, wherein said display device is incorporated into a portable information terminal.

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35. (Withdrawn) A display device according to claim 28, wherein said display device is incorporated into a car audio set.

36. (Withdrawn) A display device according to claim 28, wherein said display device is incorporated into a digital camera.

37. (Currently Amended) A display device comprising:
a gate signal line driving circuit;
~~a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;~~
a shift register included in said gate signal line driving circuit for serially outputting pulses in accordance with clock signals;
a level shifter included in said gate signal line driving circuit for converting a voltage amplitude of input signals; and
a current source provided ~~[[for]]~~ in said gate signal line driving circuit for supplying a current to said level shifter,
wherein said current source supplies the current only when said shift register serially outputs the pulses.

38. (Currently Amended) A display device according to claim 37, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

39. (Currently Amended) A display device according to claim 37, wherein said driving circuit and said pixel portion are provided over a same substrate.

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40. (Currently Amended) A display device according to claim 37, wherein said driving circuit and said pixel portion are provided over different substrates.

41. (Currently Amended) A display device according to claim 37, wherein said display device is a liquid crystal display device.

42. (Currently Amended) A display device according to claim 37, wherein said display device is incorporated into a personal computer.

43. (Currently Amended) A display device according to claim 37, wherein said display device is incorporated into a portable information terminal.

44. (Currently Amended) A display device according to claim 37, wherein said display device is incorporated into a car audio set.

45. (Currently Amended) A display device according to claim 37, wherein said display device is incorporated into a digital camera.

46. (Currently Amended) A display device comprising:
a gate signal line driving circuit;
~~a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;~~
first to $[[y_{th}]]$ y-th (y : natural number, $y \geq 2$) units included in said gate signal line driving circuit;
a shift register included in the $[[d_{th}]]$ d-th (d : natural number, $1 \leq d \leq y$) unit for serially outputting pulses in accordance with clock signals;
a plurality of level shifters included in said $[[d_{th}]]$ d-th unit for converting a voltage amplitude of input signals; and

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a [[dth]] d-th current source provided [[for]] in said [[dth]] d-th unit for supplying a current to said plurality of level shifters,

wherein said [[dth]] d-th current source supplies the current to said plurality of level shifters in said [[dth]] d-th unit only when said shift register in said [[dth]] d-th unit serially outputs the pulses.

47. (Currently Amended) A display device according to claim 46, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

48. (Currently Amended) A display device according to claim 46, wherein said driving circuit and said pixel portion are provided over a same substrate.

49. (Currently Amended) A display device according to claim 46, wherein said driving circuit and said pixel portion are provided over different substrates.

50. (Currently Amended) A display device according to claim 46, wherein said display device is a liquid crystal display device.

51. (Currently Amended) A display device according to claim 46, wherein said display device is incorporated into a personal computer.

52. (Currently Amended) A display device according to claim 46, wherein said display device is incorporated into a portable information terminal.

53. (Currently Amended) A display device according to claim 46, wherein said display device is incorporated into a car audio set.

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54. (Currently Amended) A display device according to claim 46, wherein said display device is incorporated into a digital camera.

55. (Withdrawn) A display device comprising:
a gate signal line driving circuit;
a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;
first to $[[y\text{th}]]$ y-th (y : natural number, $y \geq 2$) units included in said gate signal line driving circuit;
a shift register included in the $[[e\text{th}]]$ e-th (e : natural number, $1 < e \leq y$) unit for serially outputting pulses in accordance with clock signals;
a plurality of level shifters included in said $[[e\text{th}]]$ e-th unit for converting a voltage amplitude of input signals; and
an $[[e\text{th}]]$ e-th current source provided for said $[[e\text{th}]]$ e-th unit for supplying a current to said plurality of level shifters,
wherein said $[[e\text{th}]]$ e-th current source supplies the current to said plurality of level shifters in said $[[e\text{th}]]$ e-th unit during a part of the period in which said shift register in the e-1th unit serially outputs the pulses and only when said shift register in said $[[e\text{th}]]$ e-th unit serially outputs the pulses.

56. (Withdrawn) A display device according to claim 55, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

57. (Withdrawn) A display device according to claim 55, wherein said driving circuit and said pixel portion are provided over a same substrate.

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58. (Withdrawn) A display device according to claim 55, wherein said driving circuit and said pixel portion are provided over different substrates.

59. (Withdrawn) A display device according to claim 55, wherein said display device is a liquid crystal display device.

60. (Withdrawn) A display device according to claim 55, wherein said display device is incorporated into a personal computer.

61. (Withdrawn) A display device according to claim 55, wherein said display device is incorporated into a portable information terminal.

62. (Withdrawn) A display device according to claim 55, wherein said display device is incorporated into a car audio set.

63. (Withdrawn) A display device according to claim 55, wherein said display device is incorporated into a digital camera.

64. (Withdrawn) A display device comprising:
a gate signal line driving circuit;
a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;
first to $[[y^{th}]]$ y-th (y : natural number, $y \geq 2$) units included in said gate signal line driving circuit;
a shift register included in the $[[f^{th}]]$ f-th (f : natural number, $1 \leq f < y$) unit for serially outputting pulses in accordance with clock signals;

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a plurality of level shifters included in said $[[fth]]$ f -th unit for converting a voltage amplitude of input signals; and

an $[[fth]]$ f -th current source provided for said $[[fth]]$ f -th unit for supplying a current to said plurality of level shifters,

wherein said $[[fth]]$ f -th current source supplies the current to said plurality of level shifters in said $[[fth]]$ f -th unit during a part of the period in which said shift register in the $f+1$ th unit serially outputs the pulses and only when said shift register in said $[[fth]]$ f -th unit serially outputs the pulses.

65. (Withdrawn) A display device according to claim 64, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

66. (Withdrawn) A display device according to claim 64, wherein said driving circuit and said pixel portion are provided over a same substrate.

67. (Withdrawn) A display device according to claim 64, wherein said driving circuit and said pixel portion are provided over different substrates.

68. (Withdrawn) A display device according to claim 64, wherein said display device is a liquid crystal display device.

69. (Withdrawn) A display device according to claim 64, wherein said display device is incorporated into a personal computer.

70. (Withdrawn) A display device according to claim 64, wherein said display device is incorporated into a portable information terminal.

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71. (Withdrawn) A display device according to claim 64, wherein said display device is incorporated into a car audio set.

72. (Withdrawn) A display device according to claim 64, wherein said display device is incorporated into a digital camera.

73. (Currently Amended) A display device comprising:
a source signal line driving circuit;
~~a pixel portion, each of said source signal line driving circuit and said pixel~~
~~portion provided over a substrate;~~
a decoder included in said source signal line driving circuit for outputting pulses in accordance with input signals;
a level shifter included in said source signal line driving circuit for converting a voltage amplitude of the input signals; and
a current source provided ~~[[for]]~~ in said source signal line driving circuit for supplying a current to said level shifter,
wherein said current source supplies the current only when said decoder outputs the pulses.

74. (Currently Amended) A display device according to claim 73, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

75. (Currently Amended) A display device according to claim 73, wherein said driving circuit and said pixel portion are provided over a same substrate.

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76. (Currently Amended) A display device according to claim 73, wherein said driving circuit and said pixel portion are provided over different substrates.

77. (Currently Amended) A display device according to claim 73, wherein said display device is a liquid crystal display device.

78. (Currently Amended) A display device according to claim 73, wherein said display device is incorporated into a personal computer.

79. (Currently Amended) A display device according to claim 73, wherein said display device is incorporated into a portable information terminal.

80. (Currently Amended) A display device according to claim 73, wherein said display device is incorporated into a car audio set.

81. (Currently Amended) A display device according to claim 73, wherein said display device is incorporated into a digital camera.

82. (Currently Amended) A display device comprising:
a source signal line driving circuit;
~~a pixel portion, each of said source signal line driving circuit and said pixel~~
~~portion provided over a substrate;~~
first to [[xth]] x-th (x : natural number, $x \geq 2$) units included in said source signal
line driving circuit;
a decoder included in the [[ath]] a-th (a : natural number, $1 \leq a \leq x$) unit for
outputting pulses in accordance with input signals;
a plurality of level shifters included in said [[ath]] a-th unit for converting a voltage
amplitude of the input signals; and

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an [[ath]] a-th current source provided [[for]] in said [[ath]] a-th unit for supplying a current to said plurality of level shifters,

wherein said [[ath]] a-th current source supplies the current to said plurality of level shifters only when said decoder in said [[ath]] a-th unit outputs the pulses.

83. (Currently Amended) A display device according to claim 82, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

84. (Currently Amended) A display device according to claim 82, wherein said driving circuit and said pixel portion are provided over a same substrate.

85. (Currently Amended) A display device according to claim 82, wherein said driving circuit and said pixel portion are provided over different substrates.

86. (Currently Amended) A display device according to claim 82, wherein said display device is a liquid crystal display device.

87. (Currently Amended) A display device according to claim 82, wherein said display device is incorporated into a personal computer.

88. (Currently Amended) A display device according to claim 82, wherein said display device is incorporated into a portable information terminal.

89. (Currently Amended) A display device according to claim 82, wherein said display device is incorporated into a car audio set.

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90. (Currently Amended) A display device according to claim 82, wherein said display device is incorporated into a digital camera.

91. (Withdrawn) A display device comprising:
a source signal line driving circuit;
a pixel portion, each of said source signal line driving circuit and said pixel portion provided over a substrate;
first to $[[xth]]$ x-th (x : natural number, $x \geq 2$) units included in said source signal line driving circuit;
a decoder included in the $[[bth]]$ b-th (b : natural number, $1 < b \leq x$) unit for outputting pulses in accordance with input signals;
a plurality of level shifters included in said $[[bth]]$ b-th unit for converting a voltage amplitude of the input signals; and
a $[[bth]]$ b-th current source provided for said $[[bth]]$ b-th unit for supplying a current to said plurality of level shifters,
wherein said $[[bth]]$ b-th current source supplies the current to said plurality of level shifters in the $[[bth]]$ b-th unit only during a part of the period in which said decoder in the b-1th unit outputs the pulses and only when said decoder in said $[[bth]]$ b-th unit serially outputs the pulses.

92. (Withdrawn) A display device according to claim 91, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

93. (Withdrawn) A display device according to claim 91, wherein said driving circuit and said pixel portion are provided over a same substrate.

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94. (Withdrawn) A display device according to claim ~~[[82]]~~ 91, wherein said driving circuit and said pixel portion are provided over different substrates.

95. (Withdrawn) A display device according to claim ~~[[82]]~~ 91, wherein said display device is a liquid crystal display device.

96. (Withdrawn) A display device according to claim ~~[[82]]~~ 91, wherein said display device is incorporated into a personal computer.

97. (Withdrawn) A display device according to claim ~~[[82]]~~ 91, wherein said display device is incorporated into a portable information terminal.

98. (Withdrawn) A display device according to claim ~~[[82]]~~ 91, wherein said display device is incorporated into a car audio set.

99. (Withdrawn) A display device according to claim ~~[[82]]~~ 91, wherein said display device is incorporated into a digital camera.

100. (Withdrawn) A display device comprising:
a source signal line driving circuit;
a pixel portion, each of said source signal line driving circuit and said pixel portion provided over a substrate;
first to ~~[[xth]]~~ x-th (x : natural number, $x \geq 2$) units included in said source signal line driving circuit;
a decoder included in the ~~[[cth]]~~ c-th (c : natural number, $1 \leq c < x$) unit for outputting pulses in accordance with input signals,
a plurality of level shifters included in said ~~[[cth]]~~ c-th unit for converting a voltage amplitude of the input signals; and

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a [[cth]] c-th current source provided for said [[cth]] c-th unit for supplying a current to said plurality of level shifters,

wherein said [[cth]] c-th current source supplies the current to said plurality of level shifters in the [[cth]] c-th unit only during a part of the period in which said decoder in the c+1th unit outputs the pulses and only when said decoder in said [[cth]] c-th unit outputs the pulses.

101. (Withdrawn) A display device according to claim 100, wherein said source signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

102. (Withdrawn) A display device according to claim 100, wherein said driving circuit and said pixel portion are provided over a same substrate.

103. (Withdrawn) A display device according to claim 100, wherein said driving circuit and said pixel portion are provided over different substrates.

104. (Withdrawn) A display device according to claim 100, wherein said display device is a liquid crystal display device.

105. (Withdrawn) A display device according to claim 100, wherein said display device is incorporated into a personal computer.

106. (Withdrawn) A display device according to claim 100, wherein said display device is incorporated into a portable information terminal.

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107. (Withdrawn) A display device according to claim 100, wherein said display device is incorporated into a car audio set.

108. (Withdrawn) A display device according to claim 100, wherein said display device is incorporated into a digital camera.

109. (Currently Amended) A display device comprising:

a gate signal line driving circuit;

~~a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;~~

a decoder included in said gate signal line driving circuit for outputting pulses in accordance with input signals;

a level shifter included in said gate signal line driving circuit for converting a voltage amplitude of the input signals; and

a current source provided ~~[[for]]~~ in said gate signal line driving circuit for supplying a current to said level shifter,

wherein said current source supplies the current only when said decoder outputs the pulses.

110. (Currently Amended) A display device according to claim 109, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

111. (Currently Amended) A display device according to claim 109, wherein said driving circuit and said pixel portion are provided over a same substrate.

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112. (Currently Amended) A display device according to claim 109, wherein said driving circuit and said pixel portion are provided over different substrates.

113. (Currently Amended) A display device according to claim 109, wherein said display device is a liquid crystal display device.

114. (Currently Amended) A display device according to claim 109, wherein said display device is incorporated into a personal computer.

115. (Currently Amended) A display device according to claim 109, wherein said display device is incorporated into a portable information terminal.

116. (Currently Amended) A display device according to claim 109, wherein said display device is incorporated into a car audio set.

117. (Currently Amended) A display device according to claim 109, wherein said display device is incorporated into a digital camera.

118. (Currently Amended) A display device comprising:
a gate signal line driving circuit;
~~a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;~~

first to $[[y\text{th}]]$ y-th (y : natural number, $y \geq 2$) units included in said gate signal line driving circuit;

a decoder included in the $[[d\text{th}]]$ d-th (d : natural number, $1 \leq d \leq y$) unit for outputting pulses in accordance with input signals;

a plurality of level shifters included in said $[[d\text{th}]]$ d-th unit for converting a voltage amplitude of the input signals: and

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a ~~[[dth]]~~ d-th current source provided ~~[[for]]~~ in said ~~[[dth]]~~ d-th unit for supplying a current to said plurality of level shifters,

wherein said ~~[[dth]]~~ d-th current source supplies the current to said plurality of level shifters only when said decoder in said ~~[[dth]]~~ d-th unit outputs the pulses.

119. (Currently Amended) A display device according to claim 118, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

120. (Currently Amended) A display device according to claim 118, wherein said driving circuit and said pixel portion are provided over a same substrate.

121. (Currently Amended) A display device according to claim 118, wherein said driving circuit and said pixel portion are provided over different substrates.

122. (Currently Amended) A display device according to claim 118, wherein said display device is a liquid crystal display device.

123. (Currently Amended) A display device according to claim 118, wherein said display device is incorporated into a personal computer.

124. (Currently Amended) A display device according to claim 118, wherein said display device is incorporated into a portable information terminal.

125. (Currently Amended) A display device according to claim 118, wherein said display device is incorporated into a car audio set.

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126. (Currently Amended) A display device according to claim 118, wherein said display device is incorporated into a digital camera.

127. (Withdrawn) A display device comprising:
a gate signal line driving circuit;
a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;
first to $[[y^{th}]]$ y-th (y: natural number, $y \geq 2$) units included in said gate signal line driving circuit;
a decoder included in the $[[e^{th}]]$ e-th (e: natural number, $1 < e \leq y$) unit for outputting pulses in accordance with input signals,
a plurality of level shifters included in said $[[e^{th}]]$ e-th unit for converting a voltage amplitude of the input signals; and
an $[[e^{th}]]$ e-th current source provided for said $[[e^{th}]]$ e-th unit for supplying a current to said plurality of level shifters,
wherein said $[[e^{th}]]$ e-th current source supplies the current to said plurality of level shifters only during a part of the period in which said decoder in the e-1th unit outputs the pulses and only when said decoder in said $[[e^{th}]]$ e-th unit outputs the pulses.

128. (Withdrawn) A display device according to claim 127, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

129. (Withdrawn) A display device according to claim 127, wherein said driving circuit and said pixel portion are provided over a same substrate.

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130. (Withdrawn) A display device according to claim 127, wherein said driving circuit and said pixel portion are provided over different substrates.

131. (Withdrawn) A display device according to claim 127, wherein said display device is a liquid crystal display device.

132. (Withdrawn) A display device according to claim 127, wherein said display device is incorporated into a personal computer.

133. (Withdrawn) A display device according to claim 127, wherein said display device is incorporated into a portable information terminal.

134. (Withdrawn) A display device according to claim 127, wherein said display device is incorporated into a car audio set.

135. (Withdrawn) A display device according to claim 127, wherein said display device is incorporated into a digital camera.

136. (Withdrawn) A display device comprising:
a gate signal line driving circuit;
a pixel portion, each of said gate signal line driving circuit and said pixel portion provided over a substrate;
first to [[yth]] yth (y : natural number, $y \geq 2$) units included in said gate signal line driving circuit;
a decoder included in the [[fth]] fth ([[e:]] f: natural number, $1 \leq f < y$) unit for outputting pulses in accordance with input signals;
a plurality of level shifters included in said [[fth]] fth unit for converting a voltage amplitude of the input signals; and

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an $[[fth]]$ f -th current source provided for said $[[fth]]$ f -th unit for supplying a current to said plurality of level shifters,

wherein said $[[fth]]$ f -th current source supplies the current to said plurality of level shifters only during a part of the period in which said decoder in the $f+1$ th unit outputs the pulses and only when said decoder in said $[[fth]]$ f -th unit outputs the pulses.

137. (Withdrawn) A display device according to claim 136, wherein said gate signal line driving circuit and said pixel portion are provided over a member selected from the group consisting of a glass substrate, a plastic substrate, a stainless steel substrate and a single crystal wafer.

138. (Withdrawn) A display device according to claim 136, wherein said driving circuit and said pixel portion are provided over a same substrate.

139. (Withdrawn) A display device according to claim 136, wherein said driving circuit and said pixel portion are provided over different substrates.

140. (Withdrawn) A display device according to claim 136, wherein said display device is a liquid crystal display device.

141. (Withdrawn) A display device according to claim 136, wherein said display device is incorporated into a personal computer.

142. (Withdrawn) A display device according to claim 136, wherein said display device is incorporated into a portable information terminal.

143. (Withdrawn) A display device according to claim 136, wherein said display device is incorporated into a car audio set.

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144. (Withdrawn) A display device according to claim 136, wherein said display device is incorporated into a digital camera.

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